

Serial No. 10/734,429

AMENDMENT TO THE SPECIFICATION

Please replace the current paragraph 28 with the following:

Figure 5 shows an alternative open "circle" design of the transistor for use in a display device. In an open design, the drain is not completely encircled by the channel. A pixel 508 addressed by transistor 504 lies between the adjacent gate lines 520, 524 512, 516 and adjacent data lines 512, 516 520, 524. The transistor includes a channel 528 where the channel width shown as dotted line 532 exceeds a dimension of the pixel addressed by the transistor.

AMENDMENT TO THE CLAIMS

Please replace the currently pending claims with the following:

1. (currently amended) A thin-film transistor array of pixels comprising:
a first thin-film transistor including a channel, the channel defined by a region between a first electrode and a second electrode, the channel having a defined length and width;
a gate line coupled to a gate electrode of the first thin film transistor;
a first data line coupled to a source electrode of the first thin film transistor;
a pixel addressed by the first thin-film transistor, the pixel having two pixel dimensions including a pixel width and a pixel length, the channel width longer than the shorter of the two pixel dimensions.
2. (Original) The thin-film transistor array of claim 1 wherein the ratio of the channel width to the channel length exceeds 5.
3. (Original) The thin film transistor array of claim 1 wherein the pixel width is equal to the pixel length.
4. (Original) The thin film transistor array of claim 1 wherein the channel includes at least one bend.